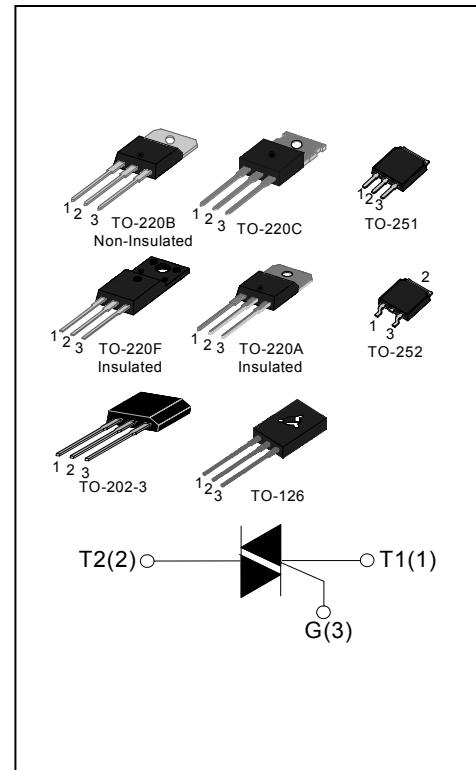


## DESCRIPTION:

With low holding and latching current, YR136 series triacs are especially recommended for use on middle and small resistance type power load. From all three terminals to external heatsink, YR136A provides a rated insulation voltage of 2500 V<sub>RMS</sub>, and YR136F provides a rated insulation voltage of 2000 V<sub>RMS</sub>, complying with UL standards.

## MAIN FEATURES

Symbol	Value	Unit
I <sub>T(RMS)</sub>	4	A
V <sub>DRM/V<sub>RRM</sub></sub>	600 and 800	V



## ABSOLUTE MAXIMUM RATINGS

Parameter	Symbol	Value	Unit
Storage junction temperature range	T <sub>stg</sub>	-40-150	°C
Operating junction temperature range	T <sub>j</sub>	-40-125	°C
Repetitive peak off-state voltage(T <sub>j</sub> =25°C)	V <sub>DRM</sub>	600/800	V
Repetitive peak reverse voltage(T <sub>j</sub> =25°C)	V <sub>RRM</sub>	600/800	V
Non repetitive surge peak Off-state voltage	V <sub>DSM</sub>	V <sub>DRM</sub> + 100	V
Non repetitive peak reverse voltage	V <sub>RSM</sub>	V <sub>RRM</sub> + 100	V
RMS on-state current	I <sub>T(RMS)</sub>	4	A
TO-251 TO-252 (T <sub>C</sub> =100°C)			
TO-220A(Ins) (T <sub>C</sub> =87°C)			
TO-220B(Non-Ins)/ TO-220C(T <sub>C</sub> =107°C)			
TO-220F(Ins) (T <sub>C</sub> =90°C)			
TO-202-3(T <sub>C</sub> =80°C)			
TO-126(T <sub>C</sub> =85°C)			

## 4A TRIACs

Non repetitive surge peak on-state current (full cycle, F=50Hz)		I <sub>TSM</sub>	35		A
I <sup>2</sup> t value for fusing (tp=10ms)		I <sup>2</sup> t	6.1		A <sup>2</sup> s
Critical rate of rise of on-state current (I <sub>G</sub> =2×I <sub>GT</sub> )	I - II - III	dI/dt	50		A/μs
	IV		10		
Peak gate current		I <sub>GM</sub>	2		A
Average gate power dissipation		P <sub>G(AV)</sub>	0.5		W
Peak gate power		P <sub>GM</sub>	5		W

### ELECTRICAL CHARACTERISTICS (T<sub>j</sub>=25°C unless otherwise specified)

Symbol	Test Condition	Quadrant		Value				Unit
				T	D	E	F	
I <sub>GT</sub>	V <sub>D</sub> =12V	I - II - III	MAX	5	5	10	25	mA
		IV		5	10	25	70	
V <sub>GT</sub>	ALL		MAX	1.3				V
V <sub>GD</sub>	V <sub>D</sub> =V <sub>DRM</sub> T <sub>j</sub> =125°C R <sub>L</sub> =3.3KΩ	ALL	MIN	0.2				V
I <sub>L</sub>	I <sub>G</sub> =1.2I <sub>GT</sub>	I - III	MAX	10	20	10	40	mA
		II - IV		15	35	20	60	
I <sub>H</sub>	I <sub>T</sub> =100mA		MAX	5	15	15	30	mA
dV/dt	V <sub>D</sub> =2/3V <sub>DRM</sub> Gate Open T <sub>j</sub> =125°C		MIN	10	20	50	50	V/μs
(dV/dt)c	(dI/dt)c=1.7A/ms T <sub>j</sub> =125°C		MIN	0.1	0.1	0.5	5	V/μs

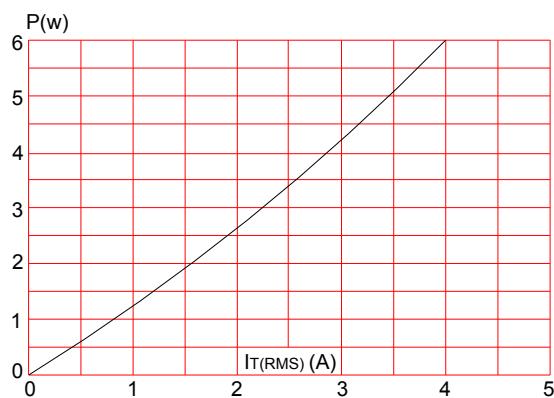
### STATIC CHARACTERISTICS

Symbol	Parameter		Value(MAX)	Unit
V <sub>TM</sub>	I <sub>TM</sub> =5.5A tp=380μs	T <sub>j</sub> =25°C	1.6	V
I <sub>DRM</sub>	V <sub>D</sub> =V <sub>DRM</sub> V <sub>R</sub> =V <sub>RRM</sub>	T <sub>j</sub> =25°C	5	μA
I <sub>RRM</sub>		T <sub>j</sub> =125°C	0.5	mA

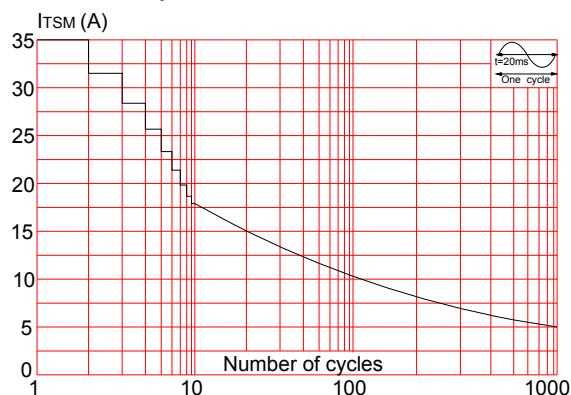
**THERMAL RESISTANCES**

<b>Symbol</b>	<b>Parameter</b>	<b>Value</b>	<b>Unit</b>
$R_{th(j-c)}$	junction to case(AC)	TO-251	°C/W
		TO-252	
		TO-220A(Ins)	
		TO-220B(Non-Ins)/ TO-220C	
		TO-220F(Ins)	
		TO-126	
	TO-202-3	3.9	

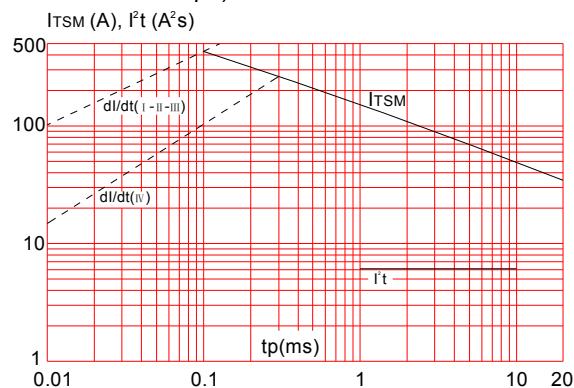
**FIG.1:** Maximum power dissipation versus RMS on-state current



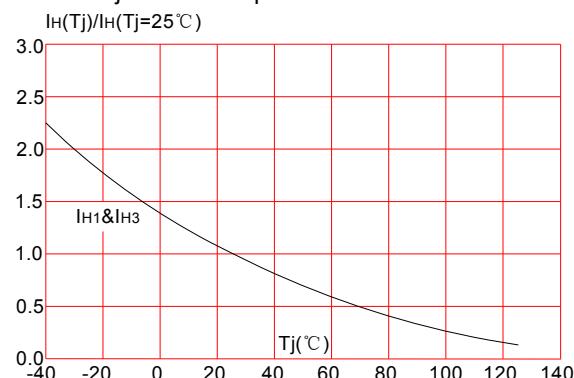
**FIG.3:** Surge peak on-state current versus number of cycles



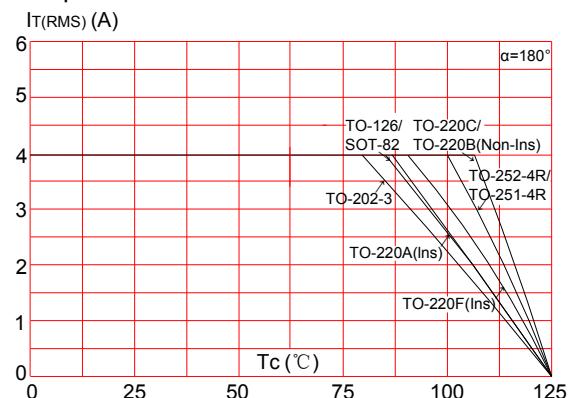
**FIG.5:** Non-repetitive surge peak on-state current for a sinusoidal pulse with width  $t_p < 20\text{ms}$  and corresponding value of  $I^2t$  ( I - II - III:  $dI/dt < 50\text{A}/\mu\text{s}$ ; IV:  $dI/dt < 10\text{A}/\mu\text{s}$ )



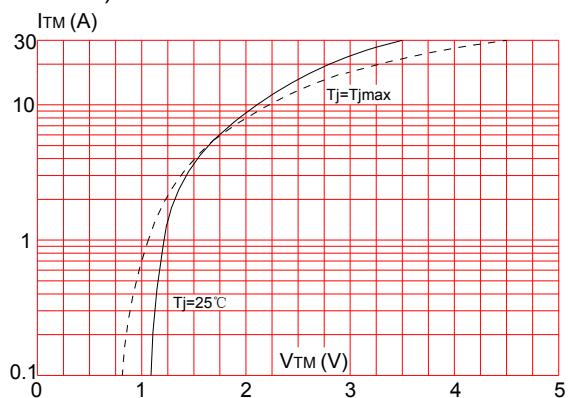
**FIG.7:** Relative variations of holding current versus junction temperature



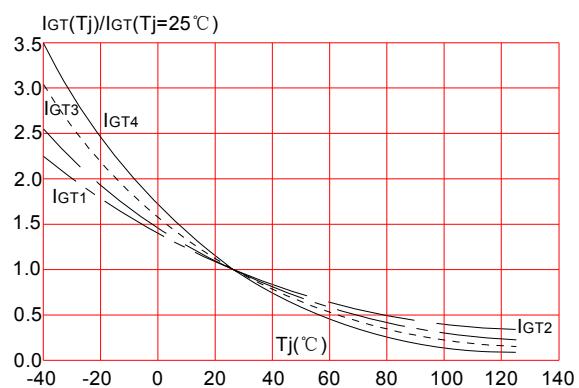
**FIG.2:** RMS on-state current versus case temperature



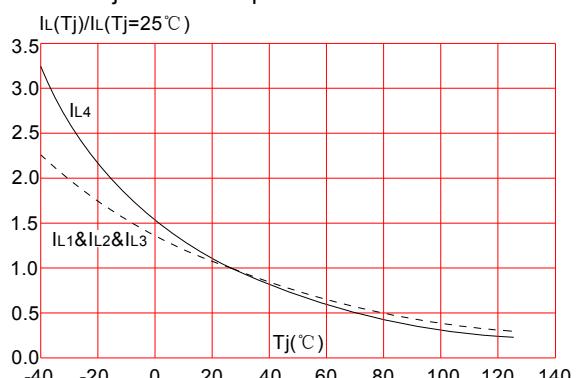
**FIG.4:** On-state characteristics (maximum values)



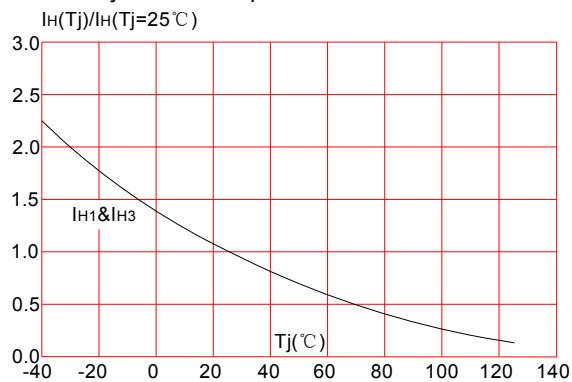
**FIG.6:** Relative variations of gate trigger current versus junction temperature



**FIG.8:** Relative variations of latching current versus junction temperature



**FIG.7:** Relative variations of holding current versus junction temperature



**FIG.8:** Relative variations of latching current versus junction temperature

